

Interference Search History

MT

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	(multi-chip near module and substrate and chip and insulating near tape).CLM.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/10/31 13:39
L2	3	(multi-chip near module and insulating near tape).CLM.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/10/31 13:39